

**REMARKS**

Claims 1, 3 and 5-7 stand rejected under 35 U.S.C. § 103(a) as being unpatentable (obvious) over Inamori '974 in view of Wyse '001.

Claims 2 and 4 stand rejected under 35 U.S.C. § 103(a) as being unpatentable (obvious) over Inamori in view of Wyse and further in view of Beaucourt '463.

Applicant respectfully traverses these rejections insofar as they may be applied to the pending amended claims 1, 3 and 5-7.

First, Applicant respectfully submits that the Inamori, Wyse and Beaucourt references disclose nothing more than the conventional prior art already acknowledged in Applicant's specification.

Applicant has no quarrel with the Examiner's summary of Inamori's disclosure appearing in the Office Action at page 2, paragraph 2. Applicant also agrees with the Examiner's following statement:

Inamori et. al. does not expressly disclose that the power amplifier is of the heterojunction bipolar transistor variety, nor does Inamori et. al. expressly disclose the use of a zero intermediate frequency architecture.

Applicant also has no quarrel with the Examiner's characterization of Wyse in the paragraph bridging paragraphs 2 and 3 of the Office Action.

However, Applicant must respectfully disagree with the Examiner's conclusions of obviousness as presented in the first two full paragraphs on page 3 of the Office Action.

Applicant also must respectfully disagree with the following Examiner's statements in the third and fourth full paragraphs on page 3 of the Office Action: "Regarding Claim 5, Inamori et. al. in view of Wyse discloses every limitation of Claim 3...", and "Regarding Claim 6, Inamori et. al. in view of Wyse discloses every limitation of Claim 3...".

Also, Applicant respectfully disagrees with the Examiner's statement (on page 3, paragraph 3, of the Office Action) in the rejection of claims 2 and 4 that "Inamori et. al. in view of Wyse discloses every limitation of Claims 1 and 3...".

Claims 2 and 4 have been canceled and their limitations inserted in independent claims 1 and 3, respectively, which have been further amended only to clarify the invention intended to be claimed.

In particular, Beaucourt does not teach, or even suggest, at least Applicant's claim limitation (claims 1 and 3),

wherein, if said output power is less than said first predetermined limit value, said input power is reduced to a value causing said control voltage to be increased to a second predetermined value where the power amplifier has only a linear said gain.

Applicant's claimed invention is limited to the very specific problem of "controlling the power delivered by a heterojunction bipolar transistor power amplifier receiving an input power and delivering an amplified output power in a zero intermediate frequency architecture" where the amplified signal is degraded at low control voltages controlling the gain of the amplifier.

Inamori, Wyse and Beaucourt do not even recognize this problem, let alone Applicant's claimed novel and unobvious solution of this problem.

Furthermore, the cited references, alone or in combination, do not disclose the fact that the power amplifier can be used in a non-linear region with a constant input power when the output power is greater than a predetermined value, but must be used in only a linear region (by reducing the input power and causing the control voltage to increase) when the output power becomes less than the predetermined value.

The above claim amendments more definitely recite the novel and unobvious steps and means for solving this problem, whereby Applicant respectfully submits that the subject matter of at least independent claims 1 and 3 would not have been *prima facie* obvious over the combined disclosures of Inamori, Wyse and Beaucourt. Furthermore, Applicant also respectfully submits that, even if the disclosures of these three references were combined in some unknown manner, there would not be produced the subject matter of any of Applicant's pending claims 1, 3 and 5-7.

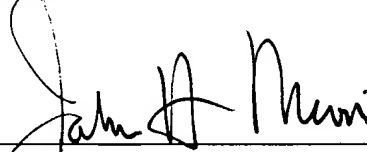
Therefore, Applicant respectfully requests the Examiner to reconsider and withdraw the three rejections under 35 U.S.C. § 103(a), and to find the application to be in condition for allowance with all of claims 1, 3 and 5-7; however, if for any reason the Examiner feels that the application is not now in condition for allowance, the Examiner is respectfully requested to **call the undersigned attorney** to discuss any unresolved issues and to expedite the disposition of the application.

Applicant hereby petitions for any extension of time which may be required to maintain the pendency of this application, and any required fee for such extension is to be charged to Deposit Account No. 19-4880. The Commissioner is also authorized to charge any additional fees

AMENDMENT UNDER 37 C.F.R. § 1.111  
U.S. APPLN. NO. 09/941,745

under 37 C.F.R. § 1.16 and/or § 1.17 necessary to keep this application pending in the Patent and  
Trademark Office or credit any overpayment to said Deposit Account No. 19-4880.

Respectfully submitted,

  
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